NOV 1 6 2005 WEAREN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	PATENT APPLICATION	
Inventor:	John Pasternak)		
)	Art Unit:	2816
Application No.:	10/633,110)		
)	Examiner:	Tra, Anh Quan
Filed:	August 1, 2003)		
)	Customer No.	28554
Title: VOLTAGE R	e: VOLTAGE REGULATOR WITH			
BYPASS FOR MULTI-VOLTAGE)		
STORAGE SYSTEM)		
_		_)		

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop RCE, Commissioner for Patents, P.Q. Box 1450, Alexandria, VA 22313-1450, on November 14, 2005

Larry E Vierra, Reg. No. 33,809 Signature Date: November 14, 2005

RESPONSE C TO FINAL OFFICE ACTION UNDER 37 C.F.R. §1.116

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This RESPONSE is in reply to the Office Action mailed May 11, 2005.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begin on page 2 of this Response.

REMARKS begin on Page 7 of this Response.

Attorney Docket No.: SAND-01013US0

sand/1013/1013.response-003